

ABSTRACT OF THE DISCLOSURE

A two-dimensional wavelet transform processing apparatus, more effectively utilizing hardware resource, is realized with reduced hardware construction. For this
5 purpose, the filter processing apparatus has a vertical DWT processor (901) for performing filter processing on image data and outputting 2 types of data obtained by the processing as 1 pair of data, a rotation unit (903) for rearranging the data outputted from the vertical DWT
10 processor by rotating the data by 90° by 2 pairs and outputting the data, and a horizontal DWT processor (905) for performing filter processing on the image data rearranged by the rotation unit and outputting 2 types of data obtained by the processing as 1 pair of data.

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